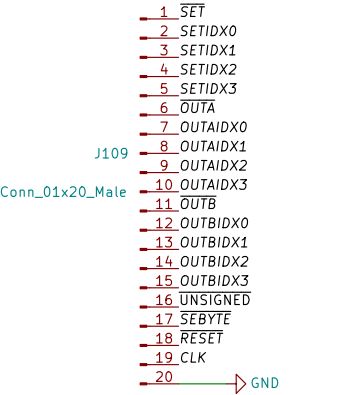
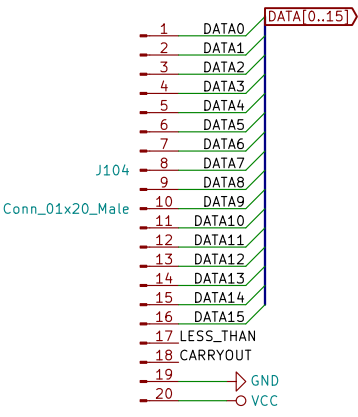
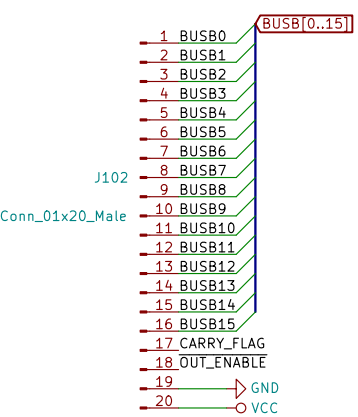
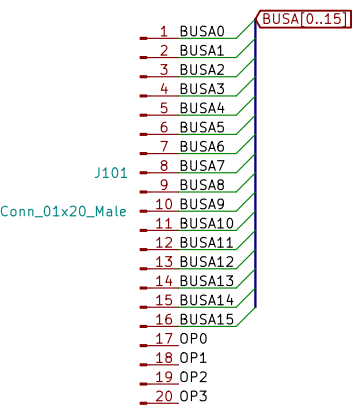
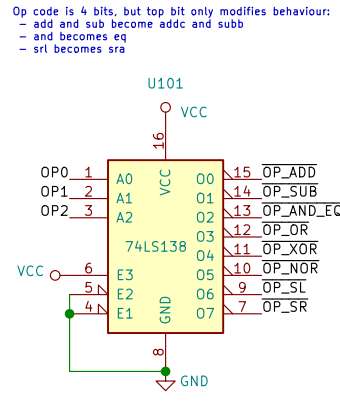


Control board connections



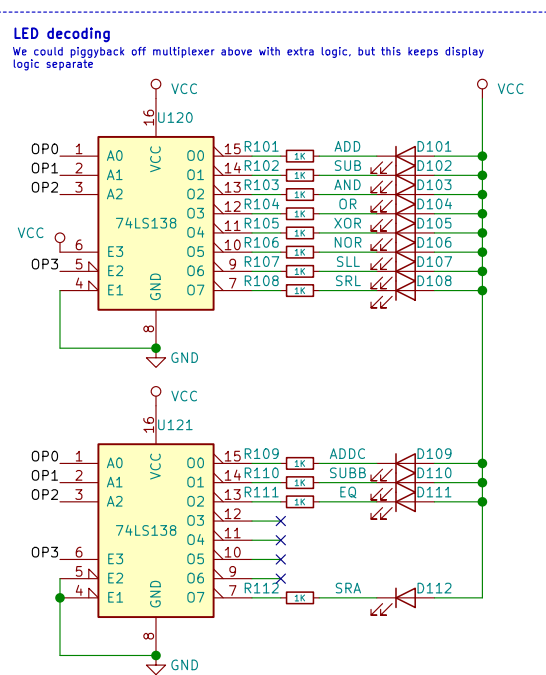
- Screw holes**
- J105 - 1x
 - J106 - 1x
 - J107 - 1x
 - J108 - 1x

Operation multiplexing

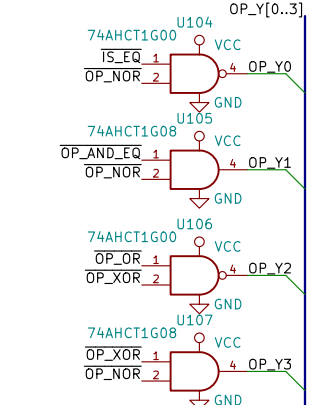


LED decoding

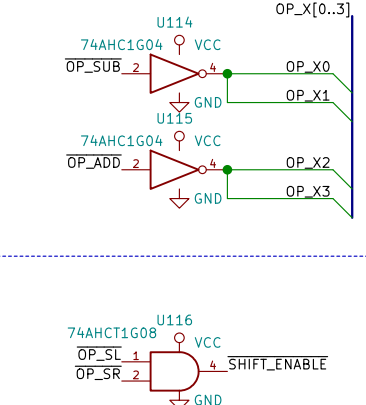
We could piggyback off multiplexer above with extra logic, but this keeps display logic separate



Y-side multiplexer input

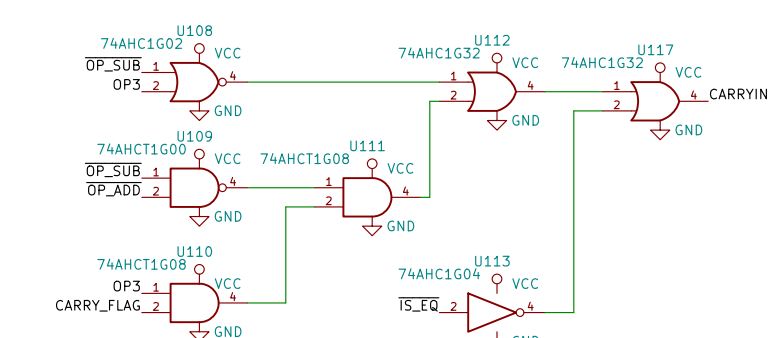


X-side multiplexer input

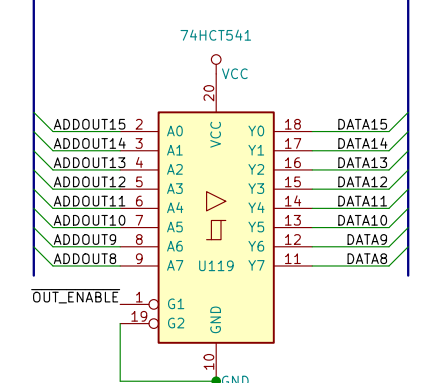
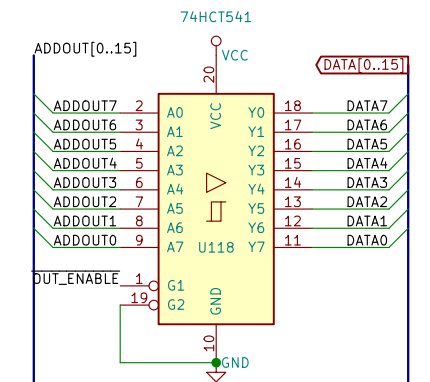
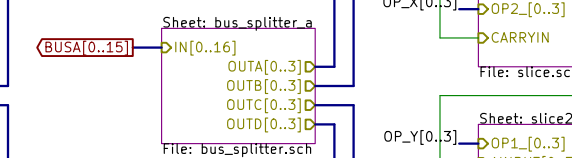
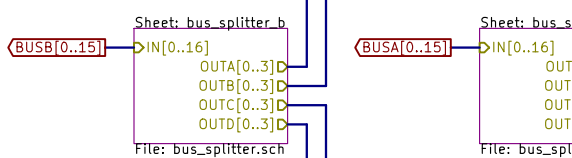
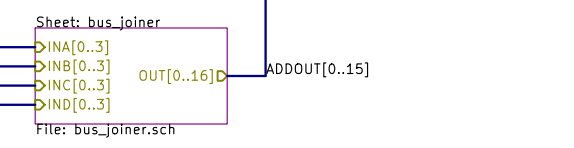
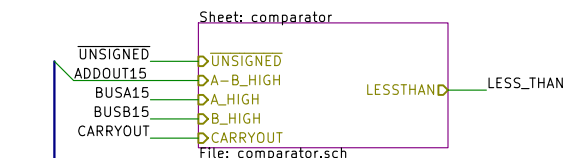
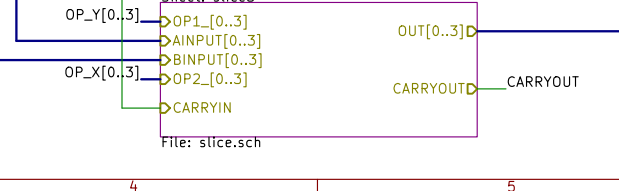
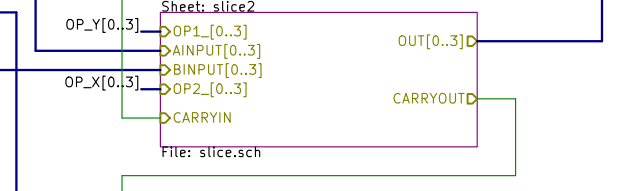
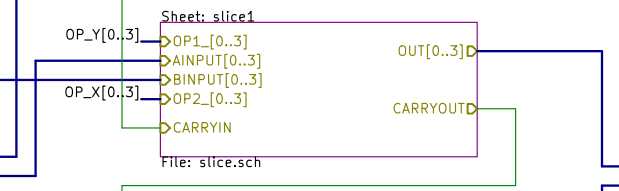
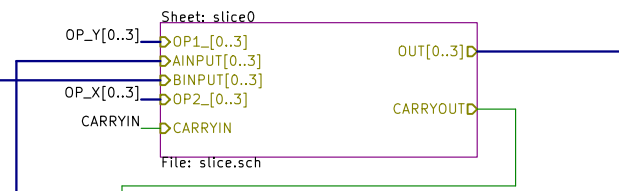
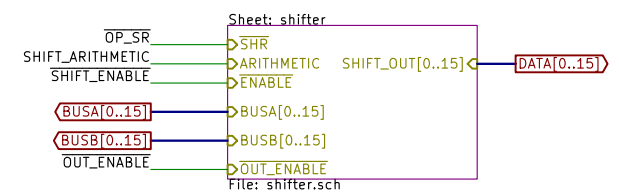
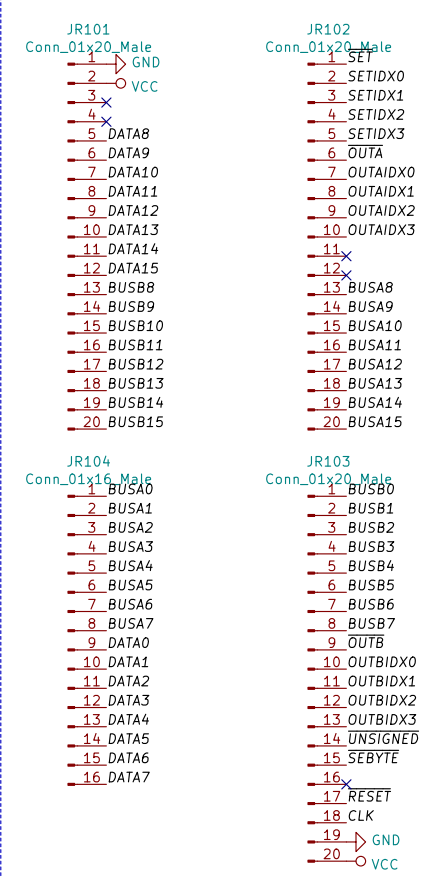


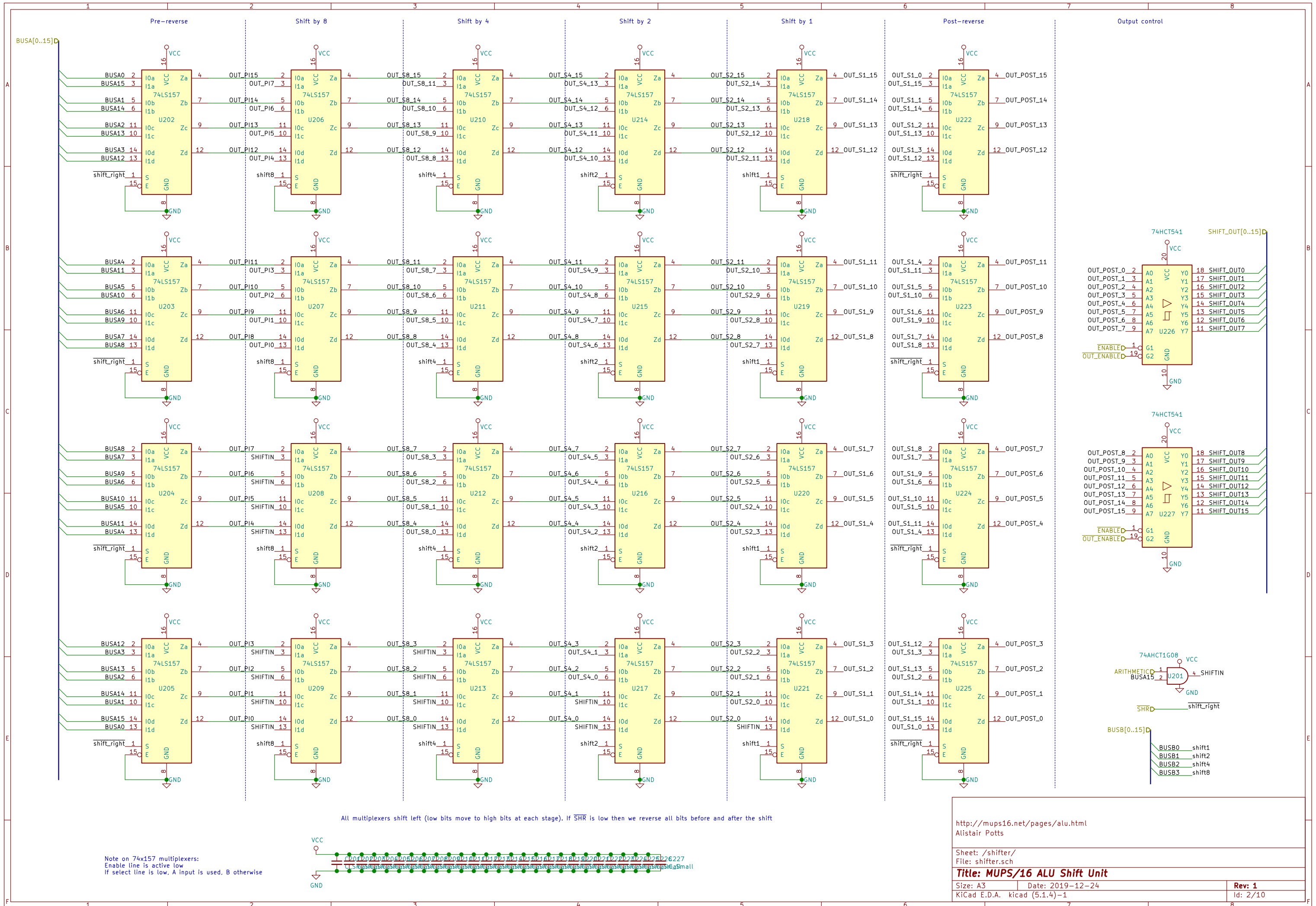
Carry-in logic

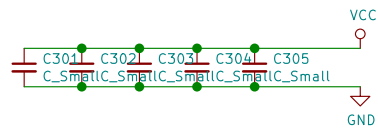
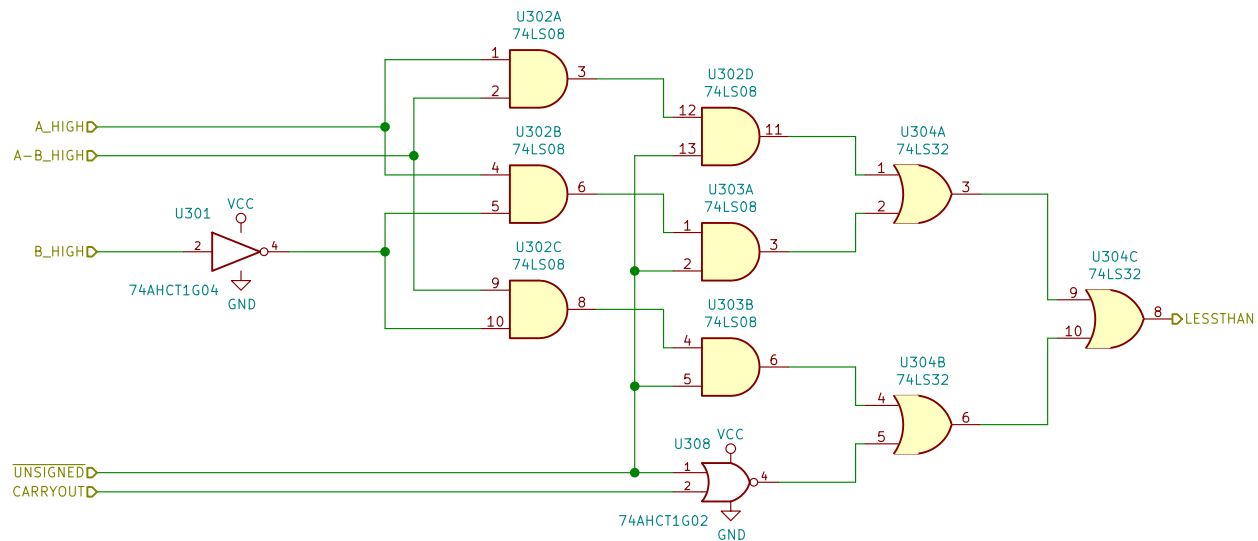
Carry in is 0 for add, 1 for sub or eq, and carry_flag for addc/subb



Register board connections







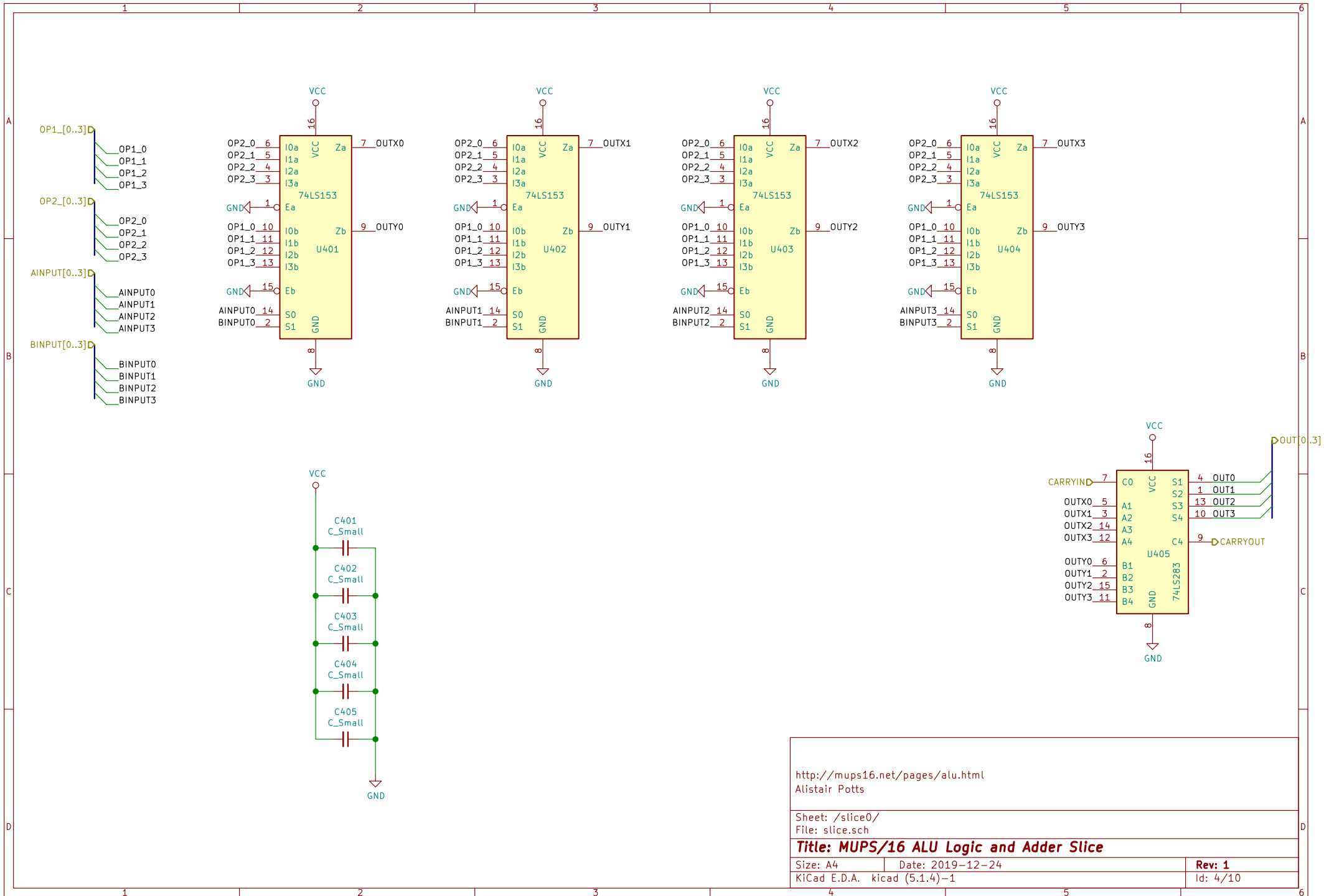
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Alistair Potts

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Size: A4 Date: 2019-12-24
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Rev: 1
Id: 3/10



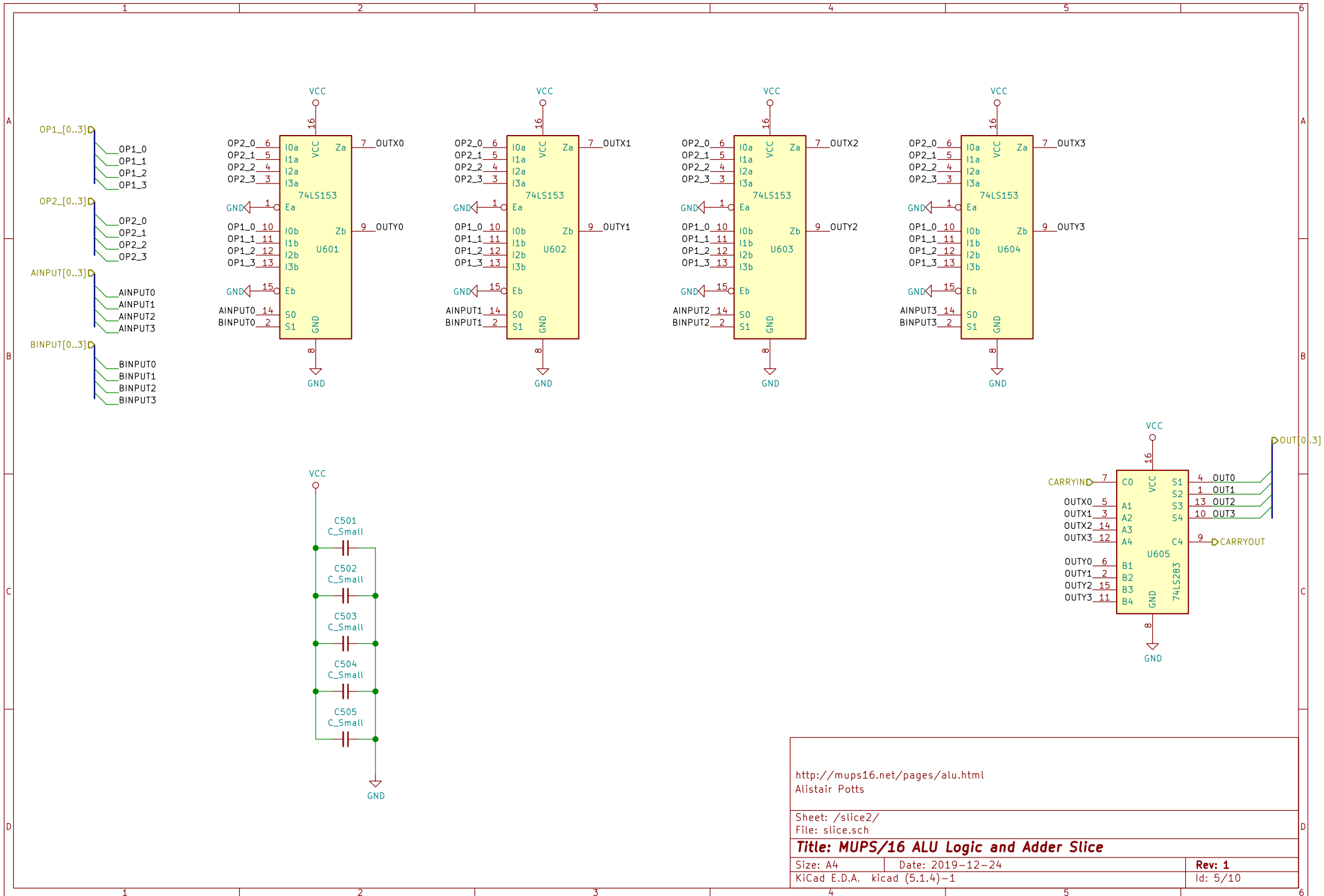
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Alistair Potts

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Rev: 1
Id: 4/10



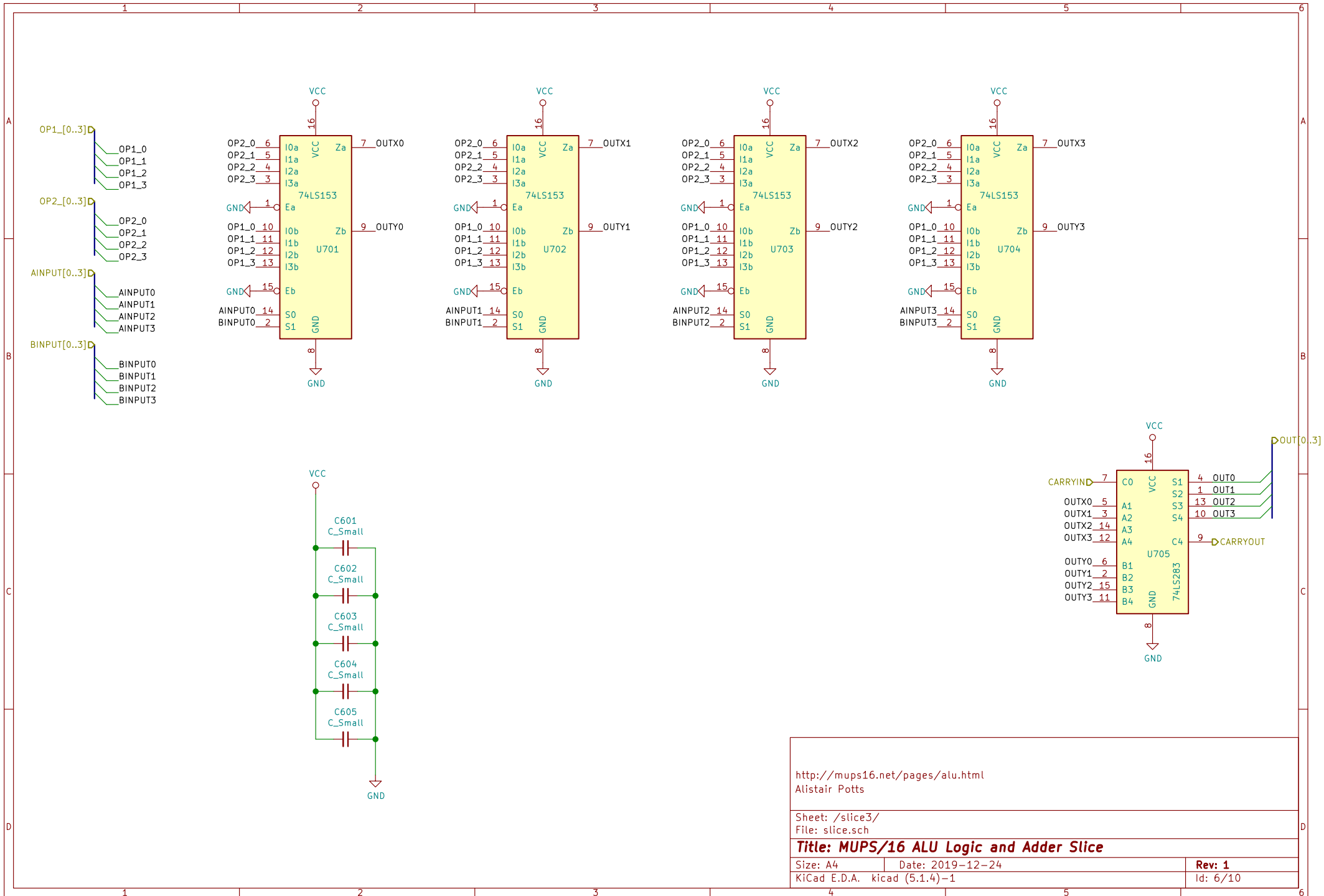
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Alistair Potts

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Rev: 1
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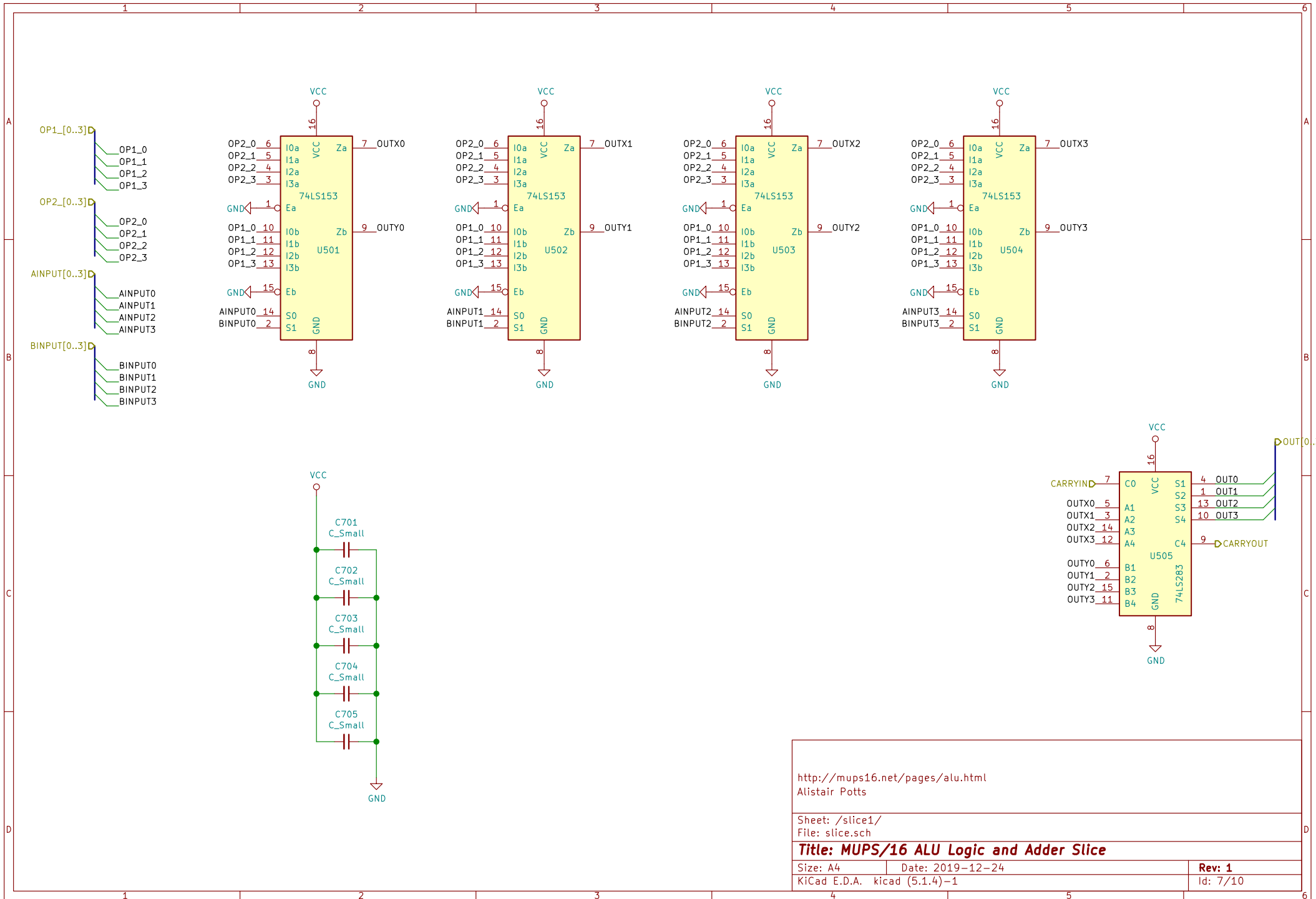
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Alistair Potts

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Rev: 1
Id: 6/10



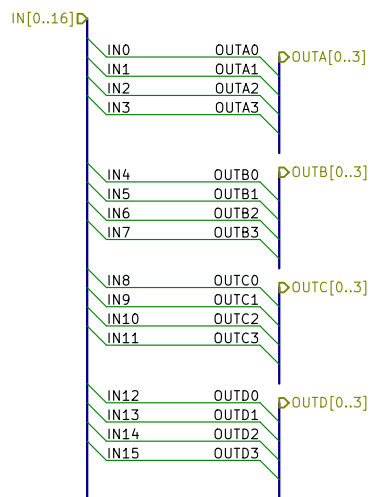
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Alistair Potts

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Title: MUPS/16 ALU Logic and Adder Slice

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Rev: 1
Id: 7/10



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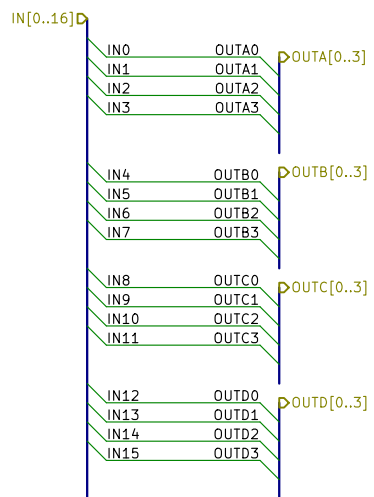
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Id: 8/10

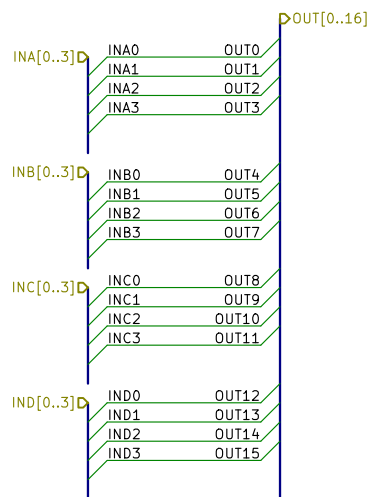


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Rev:
 Id: 9/10



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